RoHS Compliant Single Mode SFP Transceiver for GbE and Fiber Channel

FEATURES

- **Compliant with SFP Transceiver SFF-8472 MSA specification with internal calibration**
- **Compliant with Specifications for IEEE 802.3z/Gigabit Ethernet**
- **Single + 3.3V Power Supply and TTL Logic Interface**
- **EEPROM with Serial ID Functionality**
- **Laser Class 1 Product which comply with the requirements of IEC 60825-1 and IEC 60825-2**
- **Duplex LC Connector interface**

Description

The LCP-1250D4MSRBx is hot pluggable 3.3V Small-Form-Factor transceiver module designed expressly for high-speed communication applications that require rates of up to 1.25Gbit/sec. It is compliant with the Gigabit Ethernet standards, as well as the SFP Multisource Agreement (MSA).

The LCP-1250D4MSRBx provide with the LC receptacle that is compatible with the industry standard LC connector. The transceiver is also compatible with industry standard RFT connector and cage.

The post-amplifier of the LCP-1250D4MSRBx also includes a LOS (Loss Of Signal) circuit that provides a TTL logic-high output when an unusable optical signal level is detected.

The LCP-1250D4MSRBx transceiver is a Class 1 eye safety product. The optical power levels, under normal operation, are at eye safe level.

Applications

- Gigabit Ethernet
- Switch to Switch interface
- Switched backplane applications
- File server interface

Performance

- LCP-1250D4MSRBx: 1550nm DFB laser, 17dB link budget (40km)

Absolute Maximum Ratings

Parameter	Symbol	Min.	Typ.	Max.	Unit	Note
Storage Temperature	Ts	-40		85	ºC	
Supply Voltage	Vcc	0		5	V	

Recommended Operating Conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit	Note
Case Operating Temperature	Tc	-5		70	ºC	1
Supply Voltage	Vcc	3.135		3.465	V	

Note 1: See order information

Electrical Characteristics

(V_{CC}=3.3V ± 5%)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Note
Total Supply Current	ICCT			300	mA	
Transmitter						
Transmitter Differential Input Voltage	VDT	0.5		2.4	V	1
Transmitter Disable Input-High	V_{DISH}	2		V_{CC}+0.3	V	
Transmitter Disable Input-Low	V_{DISL}	0		0.8	V	
Transmitter Fault Pull up Resistor	R_{TX_FAULT}	4.7		10	kΩ	2
Transmitter Fault Output-High	V_{TXFH}	2		V_{CC}+0.3	V	2
Transmitter Fault Output-Low	V_{TXFL}	0		0.8	V	2
Receiver						
Receiver Differential Output Voltage	VDR	0.35		2	V	3
Receiver LOS Load	R_{RXLOS}	4.7		10	kΩ	2
LOS Output Voltage-High	V_{LOSH}	2		V_{CC}+0.3	V	2
LOS Output Voltage-Low	V_{LOSL}	0		0.8	V	2
Output Data Rise/Fall Time	t_r / t_f		220		psec	4

Notes:

1. Internally AC coupled and terminated to 100Ohm differential load.
2. Pull up to V_{CC} on host Board
3. Internally AC coupled, but requires a 100Ohm differential termination at or internal to Serializer/ Deserializer.
4. These are 20%~80% values

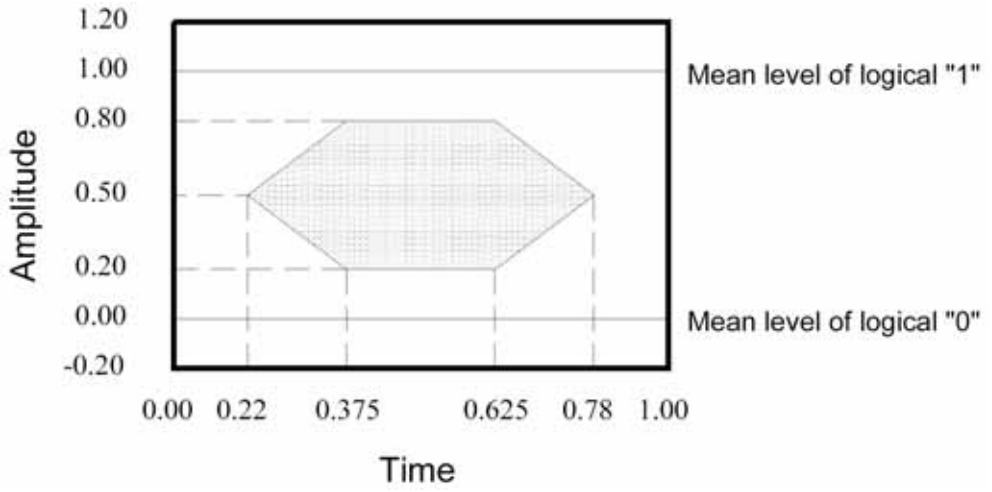
Optical Characteristics

(V_{CC}=3.3V ± 5%, Data Rate=1250Mb/sec, PRBS=2^7-1 NRZ)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Note
Transmitter						
Output Optical Power (Avg.)	P_O	-4		1	dBm	
Optical Extinction Ratio	ER	9			dB	
Center Wavelength	λ_C	1480		1580	nm	
Spectral Width (-20dB)	σ			1	nm	
Side-mode Suppression Ratio	SMSR	30			dB	
Total Jitter (pk-pk)	TJ			160	ps	
Optical Rise/ Fall Time	t_r/t_f			260	psec	1
Output Eye	Compliant with IEEE 802.3z					
Receiver						
Sensitivity (Avg.)	P_{IN}			-21	dBm	2
Input Optical Wavelength	λ	1270		1580	nm	
LOS-Deasserted (Avg.)	P_A			-21	dBm	
LOS-Asserted (Avg.)	P_D	-40			dBm	
LOS-Hysteresis	P_A-P_D	0.5			dB	
Overload	P_O	-1			dBm	

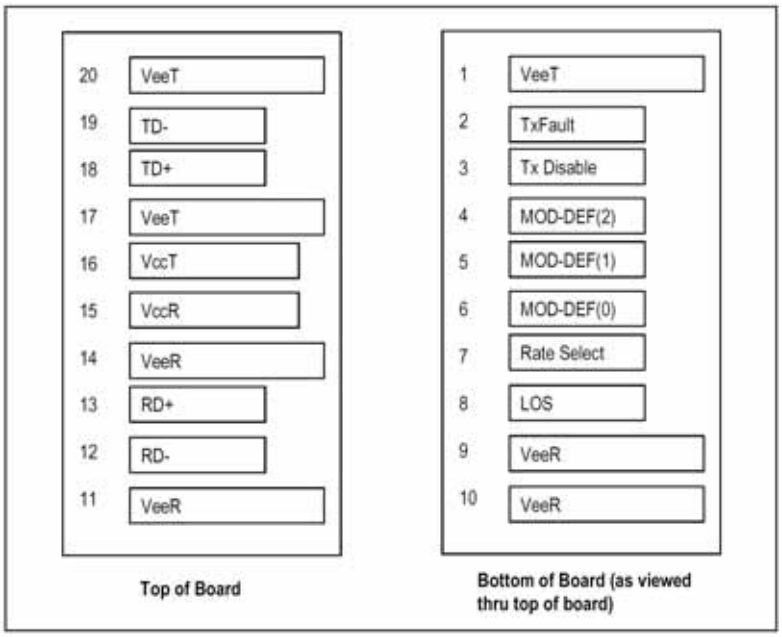
Notes:

1. These are unfiltered 20%~80% values
2. The sensitivity is provided at a BER of 1×10^{-12}or better with an input signal consisting of 1250Mb/s, 2^7-1 PRBS and ER=9dB.



Mask of the eye diagram for the optical transmit signal

SFP Transceiver Electrical Pad Layout



Pin Function Definitions

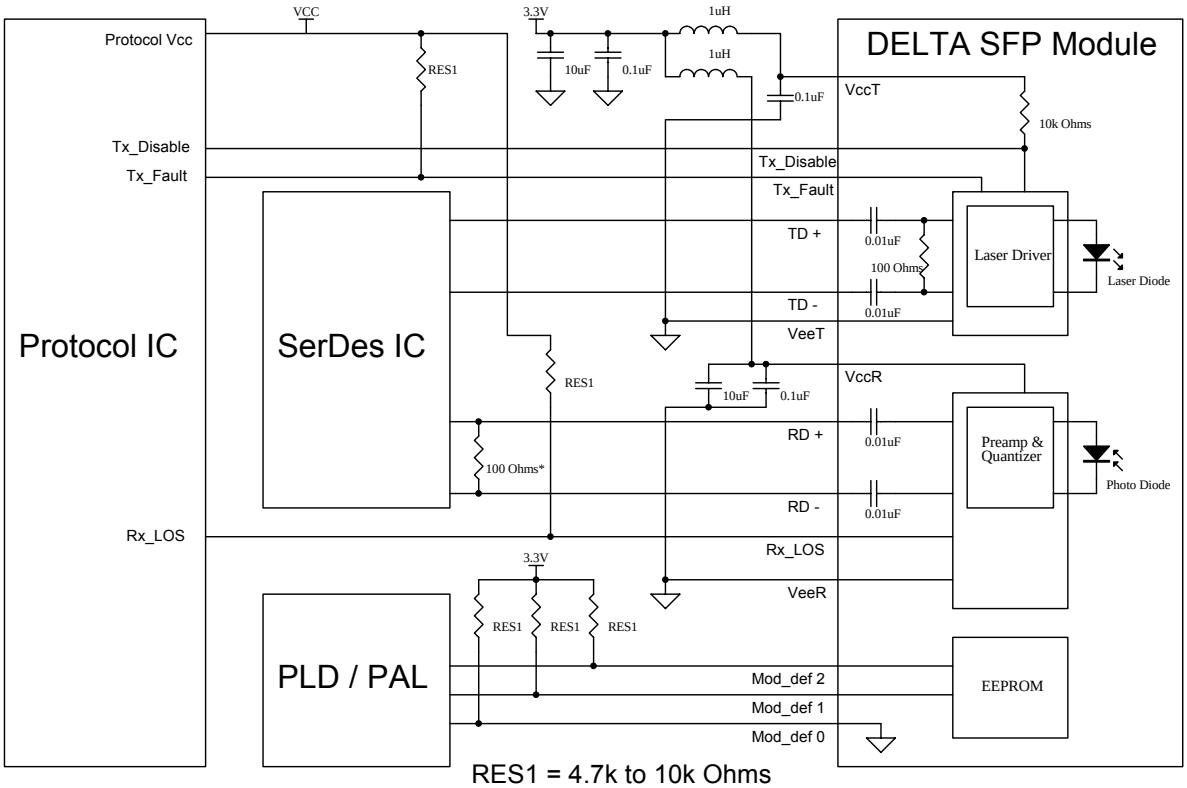
Pin Num.	Name	Function	Plug Seq.	Notes
1	VeeT	Transmitter Ground	1	
2	TX Fault	Transmitter Fault Indication	3	Note 1
3	TX Disable	Transmitter Disable	3	Note 2 Module disables on high or open
4	MOD-DEF2	Module Definition 2	3	Note 3, 2 wire serial ID interface
5	MOD-DEF1	Module Definition 1	3	Note 3, 2 wire serial ID interface
6	MOD-DEF0	Module Definition 0	3	Note 3, Grounded in Module
7	Rate Select	Not Connect	3	Function not available
8	LOS	Loss of Signal	3	Note 4
9	VeeR	Receiver Ground	1	Note 5
10	VeeR	Receiver Ground	1	Note 5
11	VeeR	Receiver Ground	1	Note 5
12	RD-	Inv. Received Data Out	3	Note 6
13	RD+	Received Data Out	3	Note 7
14	VeeR	Receiver Ground	1	Note 5
15	VccR	Receiver Power	2	3.3 ± 5%, Note 7
16	VccT	Transmitter Power	2	3.3 ± 5%, Note 7
17	VeeT	Transmitter Ground	1	Note 5
18	TD+	Transmit Data In	3	Note 8
19	TD-	Inv. Transmit Data In	3	Note 8
20	VeeT	Transmitter Ground	1	Note 5

Plug Seq.: Pin engagement sequence during hot plugging.

Notes:

1) TX Fault is an open collector/drain output, which should be pulled up with a 4.7K – 10KΩ resistor on the host board. Pull up voltage between 2.0V and VccT, R+0.3V. When high, output indicates a laser fault of some kind. Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.
2) TX disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a 4.7 – 10 K Ω resistor. Its states are:
   Low (0 – 0.8V): Transmitter on
   (>0.8, < 2.0V): Undefined
   High (2.0 – 3.465V): Transmitter Disabled
   Open: Transmitter Disabled
3) Mod-Def 0,1,2. These are the module definition pins. They should be pulled up with a 4.7K – 10KΩresistor on the host board. The pull-up voltage shall be VccT or VccR (see Section IV for further details). Mod-Def 0 is grounded by the module to indicate that the module is present Mod-Def 1 is the clock line of two wire serial interface for serial ID Mod-Def 2 is the data line of two wire serial interface for serial ID
4) LOS (Loss of Signal) is an open collector/drain output, which should be pulled up with a 4.7K – 10KΩ resistor. Pull up voltage between 2.0V and VccT, R+0.3V. When high, this output indicates the received optical power is below the worst-case receiver sensitivity (as defined by the standard in use). Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.
5) VeeR and VeeT may be internally connected within the SFP module.
6) RD-/+: These are the differential receiver outputs. They are AC coupled 100Ω differential lines which should be terminated with 100Ω (differential) at the user SERDES. The AC coupling is done inside the module and is thus not required on the host board. The voltage swing on these lines will be between 370 and 2000 mV differential (185 – 1000 mV single ended) when properly terminated.
7) VccR and VccT are the receiver and transmitter power supplies. They are defined as 3.3V ±5% at the SFP connector pin. Maximum supply current is 300mA. Recommended host board power supply filtering is shown below. Inductors with DC resistance of less than 1 ohm should be used in order to maintain the required voltage at the SFP input pin with 3.3V supply voltage. When the recommended supply-filtering network is used, hot plugging of the SFP transceiver module will result in an inrush current of no more than 30mA greater than the steady state value. VccR and VccT may be internally connected within the SFP transceiver module.
8) TD-/+: These are the differential transmitter inputs. They are AC-coupled, differential lines with 100Ω differential termination inside the module. The AC coupling is done inside the module and is thus not required on the host board. The inputs will accept differential swings of 500 – 2400 mV (250 – 1200 mV single-ended), though it is recommended that values between 500 and 1200 mV differential (250 – 600 mV single-ended) be used for best EMI performance.

Recommend Circuit Schematic

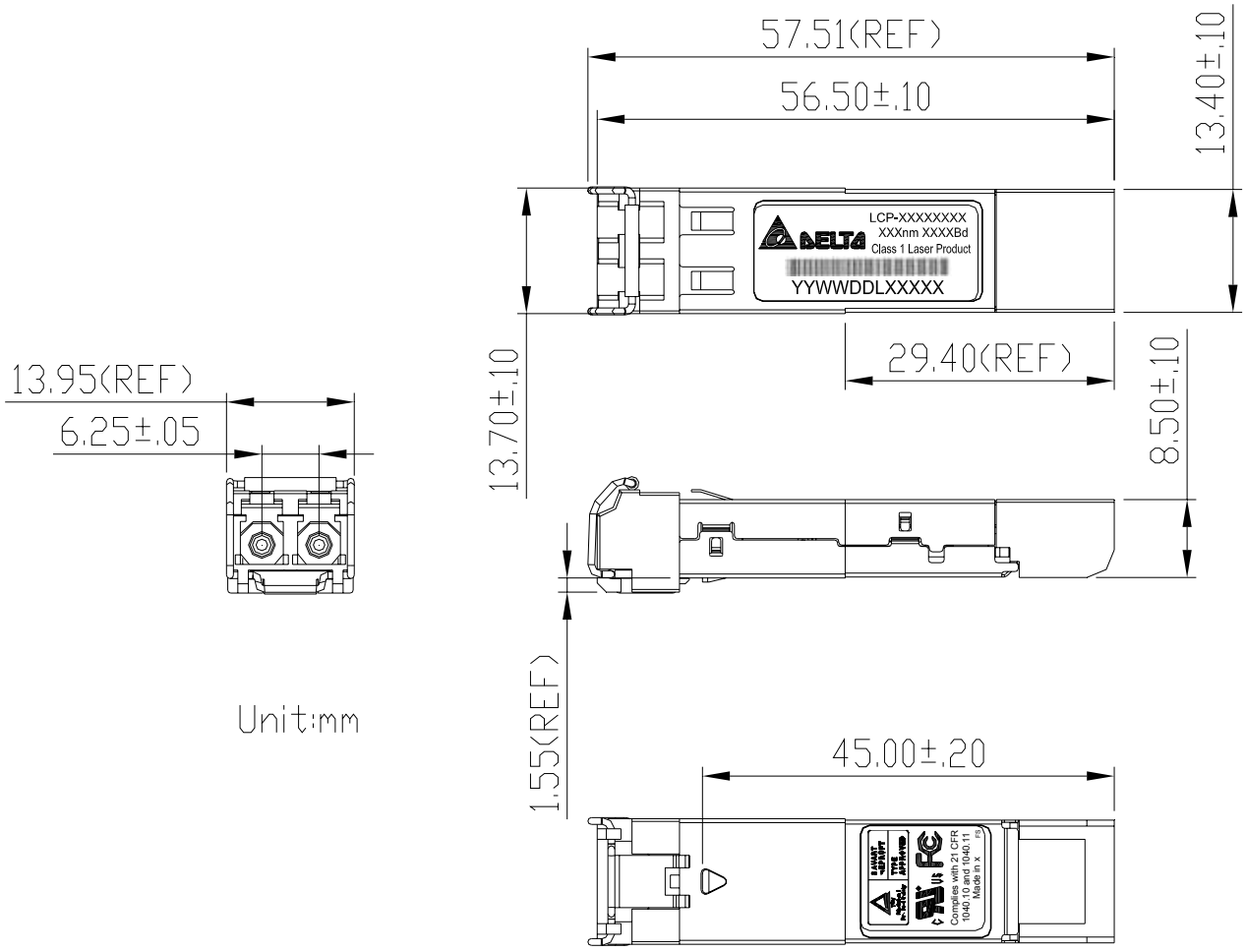


RES1 = 4.7k to 10k Ohms

* Depands on SerDes IC used

Package Outline Drawing

Latch Color Identifier
Blue



SFP timing parameters for SFP management

Parameter	Symbol	Min.	Max.	Unit	Note
TX_DISABLE Assert time	t_off		10	μsec	1
TX_DISABLE Negate time	t_on		1	msec	2
Time to initialize, including reset of TX_FAULT	t_init		300	msec	3
TX Fault Assert Time	t_fault		100	μsec	4
TX_DISABLE to reset	t_rest	10		μsec	5
LOS Assert Time	t_loss_on		100	μsec	6
LOS Deassert Time	t_loss_off		100	μsec	7
Serial ID Clock Rate	f_serial_clock		100	kHz	

Notes:

1) Time from rising edge of TX_DISABLE to when the optical output falls below 10% of nominal
2) Time from falling edge of TX_DISABLE to when the modulated optical output rises above 90% of nominal
3) From power on or negation of TX_Fault using TX Disable
4) Time from fault to TX fault on.
5) Time TX Disable must be held high to reset TX_Fault
6) Time from LOS state to Rx LOS assert
7) Time from non-LOS state to Rx LOS deassert

LCP-1250D4MSRB EEPROM Serial ID Memory Contents (2-Wire Address A0h)

Address	Hex	ASCII	Address	Hex	ASCII	Address	Hex	ASCII	Address	Hex	ASCII	Address	Hex	ASCII	Address	Hex	ASCII
00	03		25	20		50	4D	M	75	SN		100	00		125	00	
01	04		26	20		51	53	S	76	SN		101	00		126	00	
02	07		27	20		52	52	R	77	SN		102	00		127	00	Note9
03	00	Note1	28	20		53	42	B	78	SN		103	00				
04	00		29	20		54	20		79	SN		104	00				
05	00		30	20		55	20		80	SN		105	00				
06	02		31	20		56	41		81	SN		106	00				
07	00		32	20		57	20		82	SN		107	00				
08	00		33	20		58	20		83	SN		108	00				
09	00		34	20		59	20		84	DC	Note7	109	00				
10	00		35	20		60	06	Note4	85	DC		110	00				
11	01		36	00		61	0E	Note4	86	DC		111	00				
12	0D		37	00		62	00		87	DC		112	00				
13	00		38	00		63	CS1	Note5	88	DC		113	00				
14	28	Note2	39	00		64	00		89	DC		114	00				
15	FF	Note3	40	4C	L	65	1A		90	DC		115	00				
16	00		41	43	C	66	00		91	DC		116	00				
17	00		42	50	P	67	00		92	00		117	00				
18	00		43	2D	-	68	SN	Note6	93	00		118	00				
19	00		44	31	1	69	SN		94	00		119	00				
20	44	D	45	32	2	70	SN		95	CS2	Note8	120	00				
21	45	E	46	35	5	71	SN		96	00		121	00				
22	4C	L	47	30	0	72	SN		97	00		122	00				
23	54	T	48	44	D	73	SN		98	00		123	00				
24	41	A	49	34	4	74	SN		99	00		124	00				

Notes:

1) Byte 3~10: Code for electronic compatibility or optical compatibility.
2) Byte 14:Transmission distance in km
3) Byte 15: Transmission distance in 100m, distance >25.5km: FFh
4) Byte 60-61: The center wavelength of transmitter
5) Byte 63: Check sum of bytes 0-62.
6) Byte 68-83: Serial number.
7) Byte 84-91: Date code.
8) Byte 95: Check sum of bytes 64-94.
9) Byte 128 to 255 had been set hex 00.

Regulatory Compliance

Test Item	Reference	Qty'	Evaluation
(#1) Electromagnetic Interference EMC	FCC Class B EN 55022 Class B CISPR 22	5	(1) Satisfied with electrical characteristics of product spec. (2) No physical damage
(#2) Immunity： Radio Frequency Electromagnetic Field	EN 61000-4-3 IEC 1000-4-3	5	
(#3) Immunity： Electrostatic Discharge to the Duplex SC Receptacle	EN 61000-4-2 IEC 1000-4-2 IEC 801.2	5	
(#4) Electrostatic Discharge to the Electrical Pins	MIL-STD-883C Method 3015.4 EIAJ#1988.3.2B Version 2, Machine model	5	

Ordering information for SFP modules

LCP-1250D4MSRx

x: Temperature

Blank: -5 to +70 degree C
H: -10 to +85 degree C
T: -40 to +85 degree C

Revision History

Rev	Date	Contents
0A	Sep., 2007	First release
0B	Oct., 2007	1. Modify the Byte 6 of A0h from 00 to 02. 2. Add the total jitter specification of TX 3. Add latch color identifier